## UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,350,189 B1

APPLICATION NO.: 10/719904 DATED

: March 25, 2008

INVENTOR(S)

: Bartlett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Page 1 of 2

Column 1, line number 21, change "diagrams, and state-based" to --diagrams, state-based--

Column 1, line number 31, change "blocks," to --blocks;--

Column 3, line number 33, change "mode in which" to --mode, in which--

Column 4, line number 21, change "collect" to --collected--

Column 4, line number 66, change "systems," to --systems;--

Column 5, line number 14, change "blocks, that" to --blocks that--

Column 5, line number 32, change "parallel states and" to --parallel states, and--

Column 7, line number 18, change "compute" to --computes--

Column 8, line number 26, change ", such as" to --such as,--

Column 8, line number 35, change "block 350, although" to --block 350; although,--

Column 9, line number 11, change "rather" to --rather,--

Column 10, line number 37, change "such as" to --such as,--

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10, line number 46, change "initially," to --initially--

Signed and Sealed this

First Day of July, 2008.

JON W. DUDAS Director of the United States Patent and Trademark Office